TITLE: MEMORY CONFIGURATION APPARATUS, SYSTEMS, AND METHODS INVENTOR'S NAME: INCHING CHEN
SERIAL NO.: 10/815,173 DOCKET NO.: 884.897US1

REPLACEMENT SHEET

1/3



MODE OF ACCESSES	BANK 0	BANK 1	BANK 2	BANK 3
32-BIT ACCESS:	0 -> 0	0 -> 1	0 -> 2	0 -> 3
	1 → 4	1 → 5	1 → 6	1 -> 7
	2 → 8	2 -> 9	2 -> 10	2 -> 1
	3 → 12	3 → 13	3 → 14	3 → 1
64-BIT ACCESS:	0 -> 0	1 → 1	0 -> 2	1 → 3
	2 → 4	3 → 5	2 → 6	3 → 7
	4 → 8	5 → 9	4 → 10	5 -> 1
	6 → 12	7 → 13	6 → 14	7 -> 1
128-BIT ACCESS:	0 → 0	1 → 1	2 -> 2	3 -> 3
	4 → 4	5 → 5	6 → 6	7 -> 7
	8 → 8	9 -> 9	10 → 10	11 → 11
	12 → 12	13 → 13	14 → 14	15 → 15

FIG. 1

REPLACEMENT SHEET

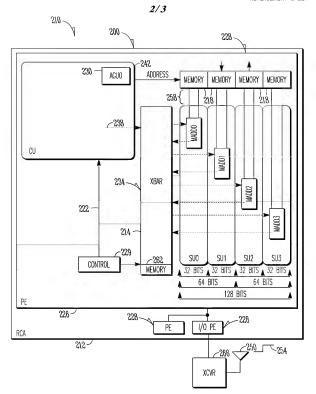


FIG. 2

SERIAL NO.: 10/815,173 DOCKET NO.: 884.B97US1

REPLACEMENT SHEET



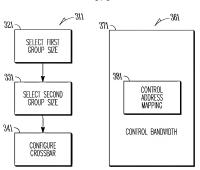


FIG. 3

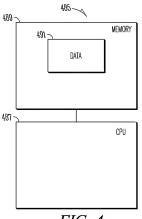


FIG. 4